



DEFENSE LOGISTICS AGENCY
LAND AND MARITIME
POST OFFICE BOX 3990
COLUMBUS, OH 43218-3990

August 3, 2016

Mr. Robert Lageman
Unicircuit Inc.
8192 Southpark Lane
Littleton, CO 80120

Dear Mr. Lageman:

Re: Qualified Manufacturers List (QML) Certification, MIL-PRF-31032; FSC: 5998; CAGE Code: 66311; VQE-16-030622 / CN051771

A validation audit was performed August 25-27, 2015, of your facilities at 8192 Southpark Lane to the requirements of MIL-PRF-31032 and your Technical Review Board (TRB) approved Quality Management (QM) Plan.

Compliance with these requirements has been successfully demonstrated. Therefore, Unicircuit Inc. is considered a certified manufacturer eligible to qualify and supply printed boards to the requirements of MIL-PRF-31032. The effective date of this certification is August 3, 2016, and covers the following:

- Quality Management Plan: Quality Policy Manual, QP-02-01 Quality Planning
- Process Flow Documentation Index: Controlled Document Master List
- Process Flow: Q-069 Technical Process Flow, Q-263 Single/Double-sided Board Process Flow

This certification gives your TRB the responsibility and flexibility to approve major changes to the MIL-PRF-31032 certified process flow and QM plan with concurrent notification to the qualifying activity (DLA Land and Maritime-VQE), with the following exceptions:

- Test optimization initiatives must be coordinated with DLA Land and Maritime-VQE prior to implementation.
- Future qualification actions initially must be coordinated with DLA Land and Maritime-VQE throughout the qualification process.

This certification is valid from the effective date of certification and can only be terminated by written notification from this Center. For the duration of the certification, the manufacturer is subject to periodic reviews by the qualifying activity or its representatives. Unicircuit Inc. agrees to submit status reports in accordance with its TRB approved QM Plan and MIL-PRF-31032.

All qualification tests, lot conformance, and periodic conformance inspections must be performed at facilities which have DLA Land and Maritime-VQ Laboratory Suitability Status for each of the applicable test methods. Should you have questions, please contact Mr. Robert Puckett, DLA Land and Maritime-VQE, at 614-692-0625 or vqe.rp@dla.mil.

Note: The results of the DLA Land and Maritime-VQE validation held on August 25-27, 2015, were found by examining a sampling of the manufacturer's normal manufacturing, testing, and processing procedures. The acceptance of the corrective actions from the above referenced validation does not relieve the manufacturer of its contractual obligation to deliver printed boards that comply with all applicable requirements.

Sincerely,

RAYMOND L KOLONCHUK
Chief
Electronic Devices Branch



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August 3, 2016

Mr. Robert Lageman
Unicircuit Inc.
8192 Southpark Lane
Littleton, CO 80120

Dear Mr. Lageman:

Re: Laboratory Suitability Status, MIL-PRF-31032 and MIL-PRF-55110; FSC: 5998; CAGE Code: 66311; VQE -16-030621 / CN051771

This office has received your correspondence, demonstrating completion of corrective actions of the findings from your most recent facilities validation. Based on this information and the validation performed August 25-27, 2015, your facility is considered suitably equipped to perform test and inspection to Department of Defense Performance Specification MIL-PRF-31032 and MIL-PRF-55110 for the following methods:

IPC-TM-650 Test Methods Manual:

| | |
|-----------------|---|
| Method 2.1.1 | Microsectioning, Manual Method |
| Method 2.1.1.2 | Microsectioning, Semi or Automatic Technique |
| Method 2.1.8 | Workmanship |
| Method 2.2.1 | Mechanical Dimension Verification |
| Method 2.2.2 | Optical Dimension Verification |
| Method 2.2.5 | Dimensional Inspections Using Microsections |
| Method 2.3.25 | Detection and Measurement of Ionizable Surface Contaminants |
| Method 2.4.1 | Plating Adhesion |
| Method 2.4.22 | Bow and Twist |
| Method 2.4.28.1 | Solder Mask Adhesion |
| Method 2.6.8 | Resistance to Soldering Heat |
| Method 2.6.26 | DC Current Induced Thermal Cycling |

J-STD-003 Solderability Tests for Printed Boards

Controlled Impedance Testing (Differential, Single-Sided)

(per manufacturer's operating procedures)

MIL-PRF-31032 All Associated Specification Sheets Test Methods:

| | |
|-------------------|-------------------------|
| Paragraph 4.7.4.1 | Marking Adhesion |
| Paragraph 4.7.5 | Electrical Test Methods |

Auto Optical Inspection (AOI) of Inner Layers

(per manufacturer's operating procedures)

Nondestructive Thickness Testing for Conductor Plating and Finish

X-Ray Fluorescence (per manufacturer's operating procedures)

This letter is not an authorization to conduct qualification tests at your plant on any of the items covered by this specification. Any additional and/or alternative test methods implemented shall be approved by the Technical Review Board (TRB) and reported to the qualifying activity as a Major Change.

Should you have questions, please contact Mr. Robert Puckett, DLA Land and Maritime-VQE, at 614-692-0625 or vqe.rp@dla.mil.

Sincerely,

RAYMOND L KOLONCHUK
Chief
Electronic Devices Branch